Type	L #	Hits	Search Text	DBs	Time Stamp	Comments	Error Definition	Errors
BRS	L1	0	(Krul-Johannes.in. Hart-Cornelius-Maria.in. DeLeeuw-Dagobert-Michel.in. DeHesse-Wilhelm-Bernardus.in. Matters-Marco.in.) and @pd>20040224	USPAT; US-PGPUB; EPO; JPO	2004/06/23 13:56			0
BRS	L2	2	(Krul-J.in. Hart-C.in. DeLeeuw-D.in. DeHesse-W.in. Matters-M.in.) and @pd>20040224	DERWENT	2004/06/23 13:56			0
BRS	L3	8	((paper cardboard) near5 (IC (integrated adj circuit) chip)) same (thread strip security forgery authentic$3) and @pd>20040224	USPAT; US-PGPUB	2004/06/23 13:57			0
BRS	L4	2	((paper cardboard) near5 (IC (integrated adj circuit) chip)) same (organic polymer) and @pd>20040224	USPAT; US-PGPUB	2004/06/23 13:59			0
BRS	L5	0	((paper cardboard currency passport) near5 (ic chip semiconduct$3)) same (security near2 (strip thread)) and @pd>20040224	USPAT; US-PGPUB	2004/06/23 13:59			0
BRS	L6	1	((paper cardboard currency passport) near5 (ic chip semiconduct$3)) and (security near2 (strip thread)) and @pd>20040224	USPAT; US-PGPUB	2004/06/23 14:00			0
BRS	L7	0	((paper cardboard currency passport) near5 (ic chip semiconduct$3)) same (organic near2 polymer) and @pd>20040224	USPAT; US-PGPUB	2004/06/23 14:03			0
BRS	L8	1	conduct$3 near5 (security near2 (strip thread)) and @pd>20040224	USPAT; US-PGPUB	2004/06/23 14:03			0
BRS	L9	1	(conduct$3 near5 (security near2 (strip thread)) and (hologram optical$2 foil kinegram) and @pd>20040224	USPAT; US-PGPUB	2004/06/23 14:04			0

Type	L #	Hits	Search Text	DBs	Time Stamp	Comments	Error Definition	Errors
BRS	L10	1	(conduct$3 near5 (security near2 (strip thread))) and (hologram optical$2 foil kinegram holographic) and @pd>20040224	USPAT; US-PGPUB	2004/06/23 14:05			0
BRS	L11	2	(security near2 (strip thread)) same (hologram holographic) and @pd>20040224	USPAT; US-PGPUB	2004/06/23 14:06			0
BRS	L12	4	((paper cardboard currency passport) near5 (ic chip semiconduct$3)) same (noncontact (non adj contact) contactlessly inductive capacitive) and @pd>20040224	USPAT; US-PGPUB	2004/06/23 14:06			0
BRS	L13	1	(ic circuit semiconductive semiconductor) same polyimide same polyaniline and @pd>20040224	USPAT; US-PGPUB	2004/06/23 14:07			0
BRS	L14	2	(organic near2 polymer) same polyimide same polyaniline and @pd>20040224	USPAT; US-PGPUB	2004/06/23 14:08			0
BRS	L15	1	(ic circuit semiconductive semiconductor) same (polyimide adj10 polyaniline) and @pd>20040224	USPAT; US-PGPUB	2004/06/23 14:09			0
BRS	L16	4	polyimide adj10 polyaniline and @pd>20040224	USPAT; US-PGPUB	2004/06/23 14:09			0
BRS	L17	8	(security near2 (strip thread)) same (size thickness width thick) and @pd>20040224	USPAT; US-PGPUB	2004/06/23 14:10			0
BRS	L18	29	(banknote$1 (bank adj note$1) passport security adj1 (document$1 paper$1)) same (IC chip circuit semiconductor semiconducting) and @pd>20040224	USPAT; US-PGPUB	2004/06/23 14:12			0

Type	L #	Hits	Search Text	DBs	Time Stamp	Comments	Error Definition	Errors
BRS	L19	0	((banknote$1 (bank adj note$1) passport (security adj1 (document$1 paper$1 thread$1 strip$1))) same (IC chip circuit semiconductor semiconducting)) not ((banknote$1 (bank adj note$1) passport (security adj1 (document$1 paper$1)) same (IC chip circuit semiconductor semiconducting)) and @pd>20040224	USPAT; US-PGPUB	2004/06/23 14:20			0
BRS	L20	15	(banknote$1 (bank adj note$1) passport (security adj1 (document$1 paper$1 thread$1 strip$1))) same (IC chip circuit semiconductor semiconducting) and @pd>20040224	EPO; JPO; DERWENT; IBM_TDB	2004/06/23 14:21			0
BRS	L21	15	(flexible flexibility bend$4) near5 (ic integrated adj circuit$1) chip semiconductor) near5 (organic carbon polymer) and @pd>20040224	USPAT; US-PGPUB	2004/06/23 14:24			0
BRS	L22	1	(flexible flexibility bend$4) near5 (ic integrated adj circuit$1) chip semiconductor) near5 (organic carbon) near5 polymer$2 and @pd>20040224	USPAT; US-PGPUB	2004/06/23 14:31			0
BRS	L23	1	(flexible flexibility bend$4) near5 (ic integrated adj circuit$1) chip semiconductor) near5 (organic carbon) near5 polymer$2 and @pd>20040224	EPO; JPO; DERWENT; IBM_TDB	2004/06/23 14:32			0
BRS	L24	3	(flexible flexibility bend$4) near5 (ic integrated adj circuit$1) chip semiconductor) near5 (organic carbon polymer) and @pd>20040224	EPO; JPO; DERWENT; IBM_TDB	2004/06/23 14:38			0

Type	L#	Hits	Search Text	DBs	Time Stamp	Comments	Error Definition	Errors
BRS	L25	0	(flexible flexibility bend$4) near5 (ic semiconductor) near5 paper and @pd>20040224	USPAT; US-PGPUB	2004/06/23 14:39			0
BRS	L26	5	(flexible flexibility bend$4) near5 (ic integrated adj circuit$1) semiconductor) near5 organic and @pd>20040224	USPAT; US-PGPUB	2004/06/23 14:39			0
BRS	L27	2	(flexible flexibility bend$4) near5 (ic integrated adj circuit$1) semiconductor) near5 organic and @pd>20040224	EPO; JPO; DERWENT; IBM_TDB	2004/06/23 14:42			0
BRS	L28	0	(flexible flexibility bend$4) near5 (ic integrated adj circuit$1) semiconductor) near5 paper and @pd>20040224	EPO; JPO; DERWENT; IBM_TDB	2004/06/23 14:42			0
BRS	L29	0	tag near5 paper near5 (ic semiconductor) near5 (antenna coil) and @pd>20040224	USPAT; US-PGPUB	2004/06/23 14:43			0
BRS	L30	3	paper near5 (ic chip semiconductor) near5 (antenna coil) and @pd>20040224	USPAT; US-PGPUB	2004/06/23 14:43			0
BRS	L31	1	tag near5 (ic chip semiconductor) near5 print$3 and (antenna coil) near5 and @pd>20040224	USPAT; US-PGPUB	2004/06/23 14:44			0
BRS	L32	508	(340/572.8.ccls. 257/774.ccls. 257/40.ccls. 361/728.ccls. 340/572.1.ccls. 340/572.7.ccls. 283/70.ccls. 162/163.ccls. 283/86.ccls. 235/451.ccls. 235/441.ccls. 283/72.ccls. 283/98.ccls. 283/83.ccls. 235/487.ccls. 235/492.ccls. 902/1.ccls. 235/441.ccls. 902/26.ccls. 902/29.ccls.) and @pd>20040224	USPAT; US-PGPUB	2004/06/23 14:45			0

Type	L#	Hits	Search Text	DBs	Time Stamp	Comments	Error Definition	Errors
1	BRS L1	0	(Krul-Johannes.in. Hart-Cornelius-Maria.in. DeLeeuw-Dagobert-Michel.in. DeHesse-Wilhelm-Bernardus.in. Matters-Marco.in.) and @pd>20040623	US-PGPUB; EPO; JPO	2004/07/01 15:19			0
2	BRS L2	0	(Krul-J.in. Hart-C.in. DeLeeuw-D.in. DeHesse-W.in. Matters-M.in.) and @pd>20040623	DERWENT	2004/07/01 15:20			0
3	BRS L3	0	((paper cardboard) near5 (IC (integrated adj circuit) chip)) same (thread strip security forgery authentic$3) and @pd>20040623	USPAT; US-PGPUB	2004/07/01 15:20			0
4	BRS L4	0	((paper cardboard) near5 (IC (integrated adj circuit) chip)) same (organic polymer) and @pd>20040623	USPAT; US-PGPUB	2004/07/01 15:22			0
5	BRS L5	0	((paper cardboard currency passport) near5 (ic chip semiconduct$3)) same (security near2 (strip thread)) and @pd>20040623	USPAT; US-PGPUB	2004/07/01 15:23			0
6	BRS L6	0	((paper cardboard currency passport) near5 (ic chip semiconduct$3)) and (security near2 (strip thread)) and @pd>20040623	USPAT; US-PGPUB	2004/07/01 15:23			0
7	BRS L7	0	((paper cardboard currency passport) near5 (ic chip semiconduct$3)) same (organic near2 polymer) and @pd>20040623	USPAT; US-PGPUB	2004/07/01 15:23			0
8	BRS L8	0	conduct$3 near5 (security near2 (strip thread)) and @pd>20040623	USPAT; US-PGPUB	2004/07/01 15:24			0
9	BRS L9	0	(conduct$3 near5 (security near2 (strip thread))) and (hologram optical$2 foil kinegram) and @pd>20040623	USPAT; US-PGPUB	2004/07/01 15:24			0

Type	L#	Hits	Search Text	DBs	Time Stamp	Comments	Error Definition	Errors
BRS	L10	0	(conduct$3 near5 (security near2 (strip thread))) and (hologram optical$2 foil kinegram holographic) and @pd>20040623	USPAT; US-PGPUB	2004/07/01 15:24			0
BRS	L11	1	(security near2 (strip thread)) same (hologram holographic) and @pd>20040623	USPAT; US-PGPUB	2004/07/01 15:25			0
BRS	L12	0	((paper cardboard currency passport) near5 (ic chip semiconduct$3)) same (noncontact (non adj contact) contactlessly inductive capacitive) and @pd>20040623	USPAT; US-PGPUB	2004/07/01 15:25			0
BRS	L13	1	(ic circuit semiconductive semiconductor) same polyimide same polyaniline and @pd>20040623	USPAT; US-PGPUB	2004/07/01 15:33			0
BRS	L14	0	(organic near2 polymer) same polyimide same polyaniline and @pd>20040623	USPAT; US-PGPUB	2004/07/01 15:34			0
BRS	L15	0	(ic circuit semiconductive semiconductor) same (polyimide adj10 polyaniline) and @pd>20040623	USPAT; US-PGPUB	2004/07/01 15:34			0
BRS	L16	0	polyimide adj10 polyaniline and @pd>20040623	USPAT; US-PGPUB	2004/07/01 15:34			0
BRS	L17	0	(security near2 (strip thread)) same (size thickness width thick) and @pd>20040623	USPAT; US-PGPUB	2004/07/01 15:35			0
BRS	L18	2	(banknote$1 (bank adj note$1) passport (security adj1 (document$1 paper$1)) same (IC chip circuit semiconductor semiconducting) and @pd>20040623	USPAT; US-PGPUB	2004/07/01 15:35			0

Type	L#	Hits	Search Text	DBs	Time Stamp	Comments	Error Definition	Errors
BRS	L20	0	((banknote$1 (bank adj note$1) passport (security adj1 (document$1 paper$1 thread$1 strip$1))) same (IC chip circuit semiconductor semiconducting)) not ((banknote$1 (bank adj note$1) passport (security adj1 (document$1 paper$1))) same (IC chip circuit semiconductor semiconducting)) and @pd>2004062 3	USPAT; US-PGPUB	2004/07/01 15:41			0
BRS	L21	0	((banknote$1 (bank adj note$1) passport (security adj1 (document$1 paper$1 thread$1 strip$1))) same (IC chip circuit semiconductor semiconducting) and @pd>2004062 3	EPO; JPO; DERWENT; IBM_TDB	2004/07/01 15:41			0
BRS	L22	3	(flexible flexibility bend$4) near5 (ic semiconductor) near5 (organic carbon polymer) and @pd>2004062 3	USPAT; US-PGPUB	2004/07/01 15:42			0
BRS	L23	0	(flexible flexibility bend$4) near5 (ic semiconductor) near5 (organic carbon) near5 polymer$2 and @pd>2004062 3	USPAT; US-PGPUB	2004/07/01 15:46			0
BRS	L24	0	(flexible flexibility bend$4) near5 (ic (integrated adj circuit$1) chip semiconductor) near5 (organic carbon) near5 polymer$2 and @pd>2004062 3	EPO; JPO; DERWENT; IBM_TDB	2004/07/01 15:46			0
BRS	L25	0	(flexible flexibility bend$4) near5 (ic (integrated adj circuit$1) chip semiconductor) near5 (organic carbon polymer) and @pd>2004062 3	EPO; JPO; DERWENT; IBM_TDB	2004/07/01 15:47			0

TYPe	L #	Hits	Search Text	DBs	Time Stamp	Comments	Error Definition	Errors
BRS	L26	0	(flexible flexibility bend$4) near5 (ic semiconductor) near5 paper and @pd>20040623	USPAT; US-PGPUB	2004/07/01 15:47			0
BRS	L27	1	(flexible flexibility bend$4) near5 (ic integrated adj circuit$1) semiconductive) near5 organic and @pd>20040623	USPAT; US-PGPUB	2004/07/01 15:47			0
BRS	L28	0	(flexible flexibility bend$4) near5 (ic integrated adj circuit$1) semiconductive) near5 organic and @pd>20040623	EPO; JPO; DERWENT; IBM_TDB	2004/07/01 15:48			0
BRS	L29	0	(flexible flexibility bend$4) near5 (ic integrated adj circuit$1) semiconductor) near5 paper and @pd>20040623	EPO; JPO; DERWENT; IBM_TDB	2004/07/01 15:48			0
BRS	L30	0	tag near5 paper near5 (ic chip semiconductor) near5 (antenna coil) near5 @pd>20040623	USPAT; US-PGPUB	2004/07/01 15:48			0
BRS	L31	1	paper near5 (ic chip semiconductor) near5 (antenna coil) and @pd>20040623	USPAT; US-PGPUB	2004/07/01 15:49			0
BRS	L32	0	tag near5 (ic chip semiconductor) near5 (antenna coil) near5 print$3 and @pd>20040623	USPAT; US-PGPUB	2004/07/01 15:49			0
BRS	L33	52	(340/572.8.ccls. 257/774.ccls. 257/40.ccls. 361/728.ccls. 340/572.1.ccls. 340/572.7.ccls. 283/70.ccls. 162/163.ccls. 283/86.ccls. 235/451.ccls. 235/441.ccls. 283/72.ccls. 283/98.ccls. 283/83.ccls. 235/487.ccls. 235/492.ccls. 902/1.ccls. 235/492.ccls. 902/26.ccls. 902/29.ccls.) and @pd>20040623	USPAT; US-PGPUB	2004/07/01 15:50			0